

Class	Subclass
ISSUE CLASSIFICATION	


**PATENT NUMBER**

**U.S. UTILITY Patent Application**

**O.I.P.E.**

**PATENT DATE**

FA  
SCANNED 101 Q.A. EW

APPLICATION NO. 09/881226	CONT/PRIOR D	CLASS <del>326</del> 71	SUBCLASS 149	ART UNIT <del>2849</del> 218	EXAMINER 
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## APPLICANTS

Roger May  
Andrew Draper  
Paul Metzgen  
Neil Thorne

Embedded processor with dual-port SRAM for programmable logic

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PTO-2040  
12/89

<b>ISSUING CLASSIFICATION</b>								
<b>ORIGINAL</b>				<b>CROSS REFERENCE(S)</b>				
<b>CLASS</b>		<b>SUBCLASS</b>		<b>CLASS</b>	<b>SUBCLASS (ONE SUBCLASS PER BLOCK)</b>			
<b>INTERNATIONAL CLASSIFICATION</b>								

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<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>			<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.  <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____    	_____ (Assistant Examiner) (Date)			<b>NOTICE OF ALLOWANCE MAILED</b>	
	_____ (Primary Examiner) (Date)			<b>ISSUE FEE</b>	
				Amount Due	Date Paid
<input type="checkbox"/> The terminal ____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)			<b>ISSUE BATCH NUMBER</b>	

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